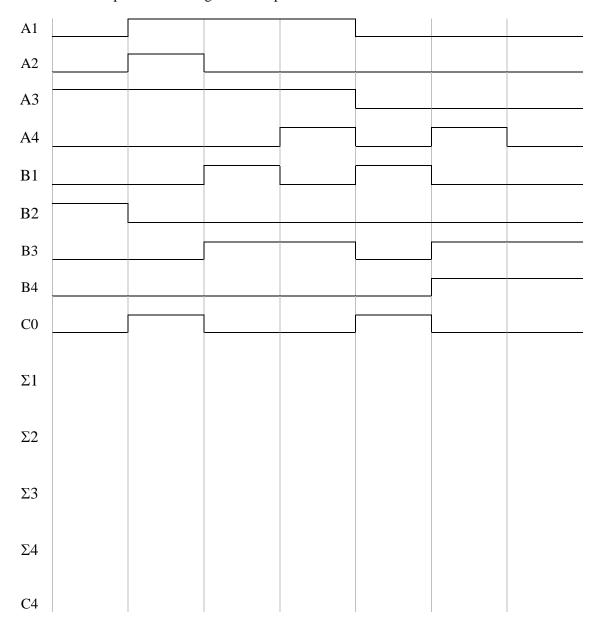
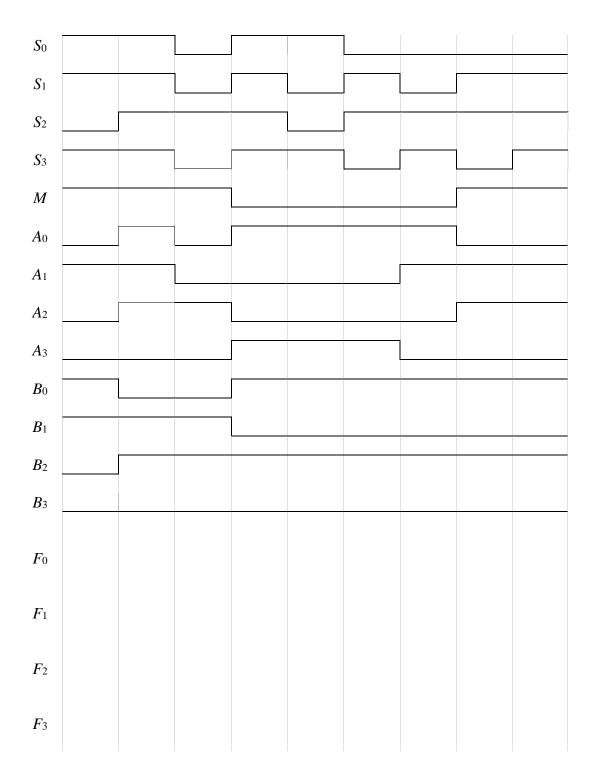
## 1. Perform these additions of binary numbers.

## 2. Draw the output waveforms given the inputs shown below to a 74283 adder.



3. Draw the output waveforms for a 74181 arithmetic logic unit. Assume  $\overline{C}_n$  is a constant HIGH.



4.	. What is the one's-complement of 0011 0101 <sub>2</sub> ?				
5.	What is the two's-comp	plement of 0011 0	101 <sub>2</sub> ?		
6.	In the table below, list a convert each one to dec	_	ry four-bit intege	rs in increasing order, and	
		Binary	Decimal		

7. What range of unsigned integers can we represent using 16 bits?

8. What range of unsigned integers can we represent using 32 bits?

9. In the table below, list all **signed** binary four-bit integers in increasing order, and convert each one to decimal.

Binary	Decimal

- 10. What range of signed integers can we represent using 16 bits?
- 11. What range of signed integers can we represent using 32 bits?

Converting from Decimal to Two's-Complement Form:  12. Convert 23 <sub>10</sub> to two's complement form using 8 bits.
13. Convert -23 <sub>10</sub> to two's complement form using 8 bits
14. Convert 58 <sub>10</sub> to two's complement form using 8 bits.
14. Convert 5010 to two 5 complement form using 6 ons.
15. Convert -58 <sub>10</sub> to two's complement form using 8 bits
Converting from Two's-Complement Form to Decimal:  16. Convert 0110 0010 <sub>2</sub> from two's complement form to decimal.
17. Convert 1110 0010 <sub>2</sub> from two's complement form to decimal.
18. Convert 0001 1010 <sub>2</sub> from two's complement form to decimal.
19 Convert 1011 10102 from two's complement form to decimal